



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Chen *et al.*

Appl. No.: 10/643,957

Filed: August 20, 2003

For: **High Voltage Power Management  
Unit Architecture in CMOS Process**

Confirmation No.: 4983

Art Unit: 2838

Examiner: Patel, Rajnikant B.

Atty. Docket: 1875.4170000/JTH/GSB

**Reply Under 37 C.F.R. § 1.111**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated **February 10, 2005**, Applicants submit the following Remarks. This Reply is provided in the following format:

- (A) Each section begins on a separate sheet;
- (B) Starting on a separate sheet, the Remarks.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefore (including fees for net addition of claims) are hereby authorized to be charged to our Deposit Account No. 19-0036.